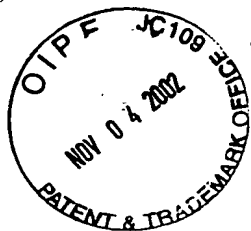


Fig. 1

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21
22
23
14 a
24

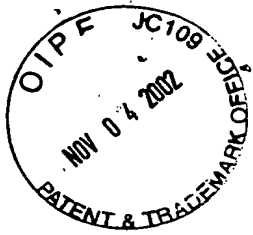
Current Bus Owner	Next Bus Owner	Number of Misses	Actual Bus Owner

*Fig. 2*

21
22
23
14 a
24

Current Bus Owner	Next Bus Owner	Number of Misses	Actual Bus Owner
C	A	0	A
A	B	1	C
B	D	0	D
D	H	2	G
H	J	0	J
E	F	0	F
G	H	3	I
F	B	0	B
J	A	0	A

*Fig. 3*



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Fig. 4

14b

Current Sequence Number	Processor Sequence	Next Sequence Number	Number of Misses	Actual Processor Sequence

14b

Current Sequence Number	Processor Sequence	Next Sequence Number	Number of Misses	Actual Processor Sequence
1	CACBA	1	0	CACBA
2	BABDH	3	1	BABDE
3	DADGE	5	0	DADGE
4	HEHAC	9	2	HEHAB
5	ACFEG	1	0	ACFEG

Fig. 5

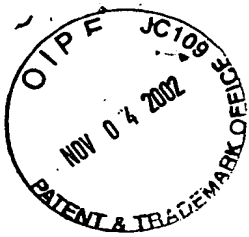


Fig. 6

